CPE301 – SPRING 2019

MIDTERM 1

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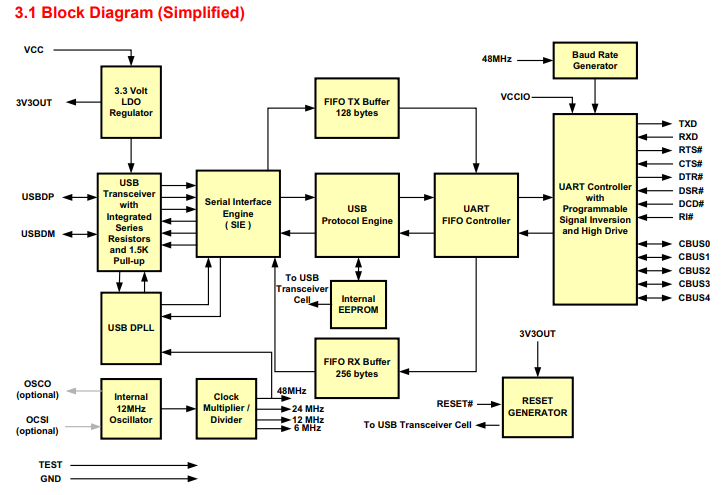
Student Email: zunigc2@unlv.nevada.edu

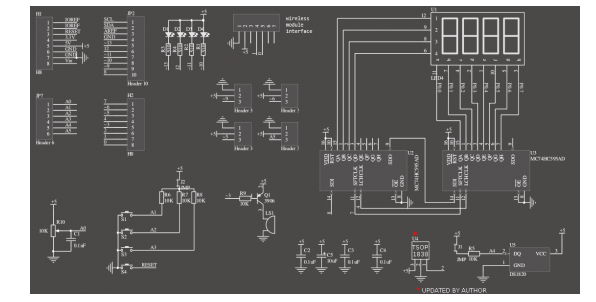
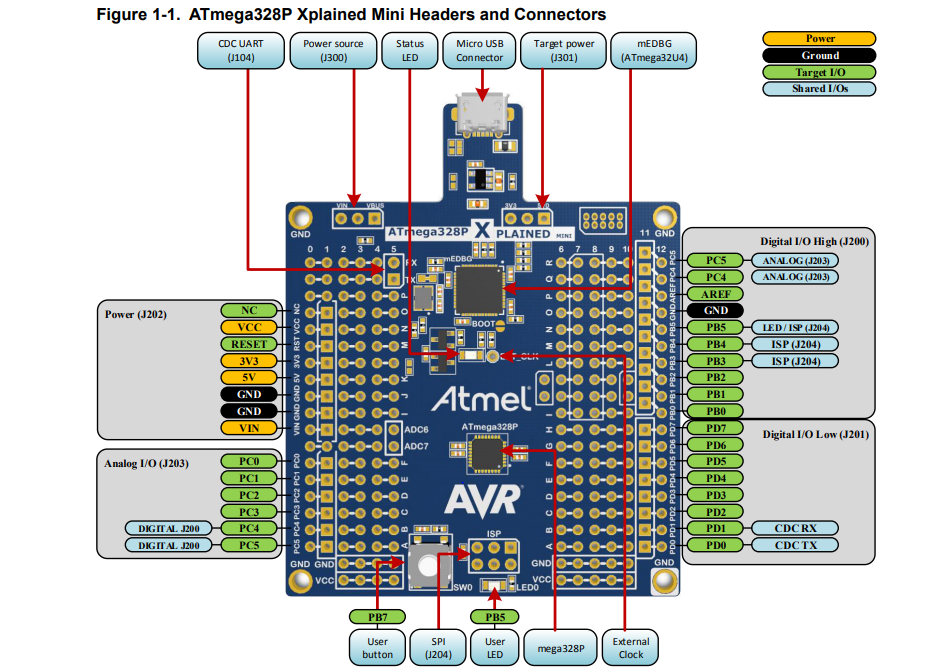
Primary Github address: https://github.com/cdaniel64/blassen/

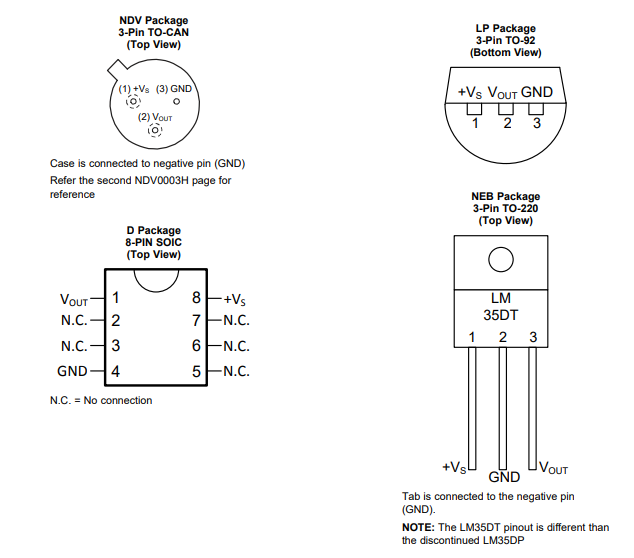
Directory: Midterms/Midterm 1

1. **COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS**

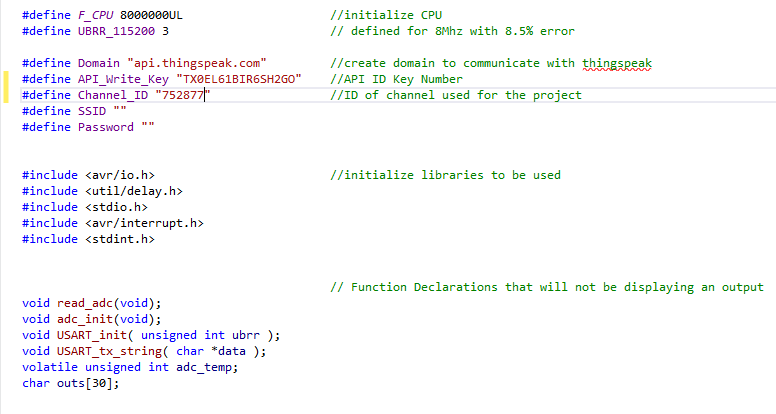
List of Components used

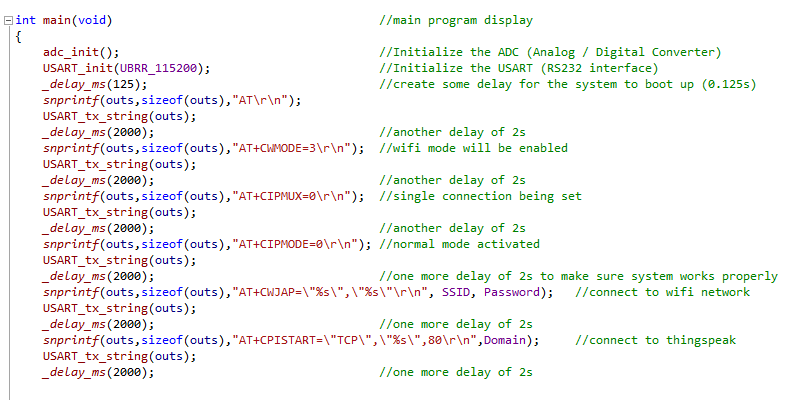
ATMEGA328P, HITLEGO Shield, and HITLEGO FTDR, LM35 temperature sensor



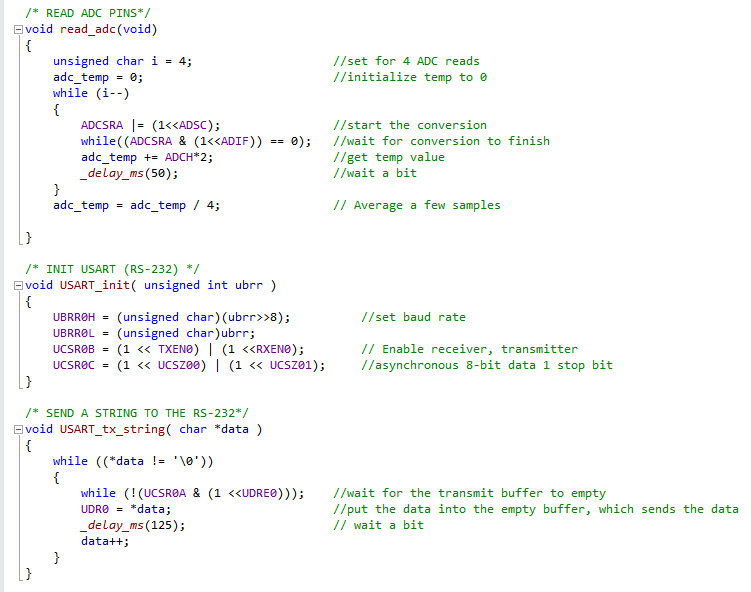


1. **INITIAL/MODIFIED/DEVELOPED CODE OF TASKS**

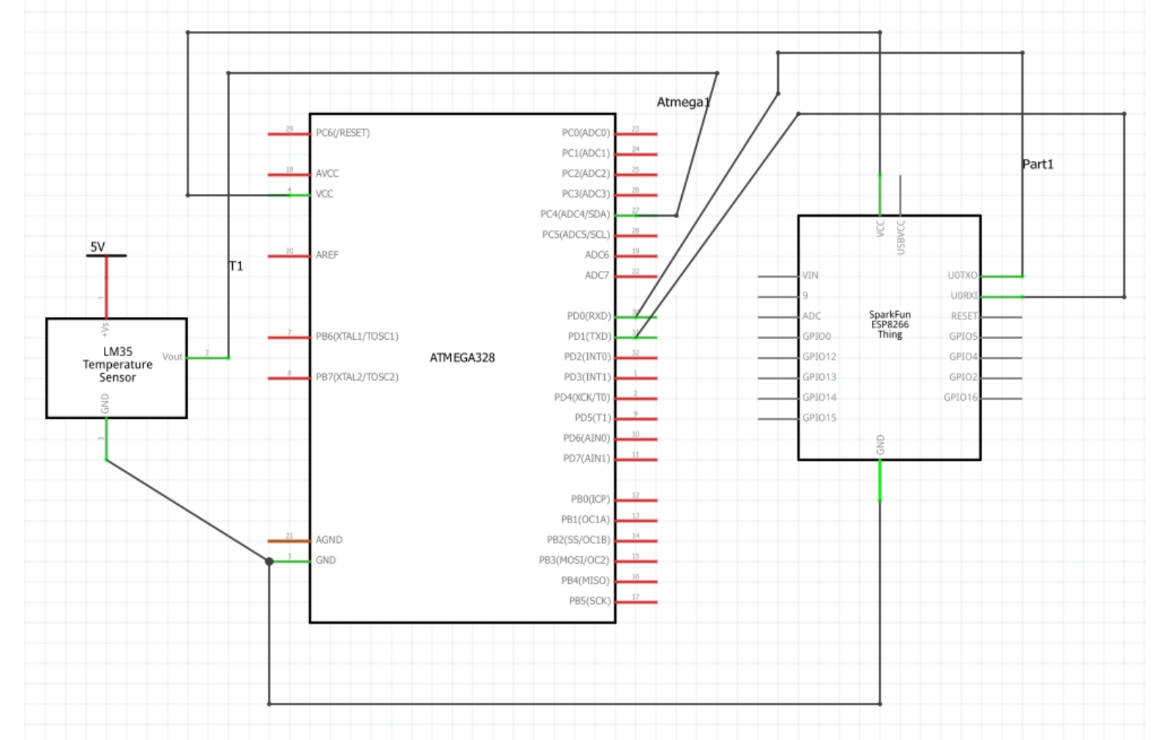




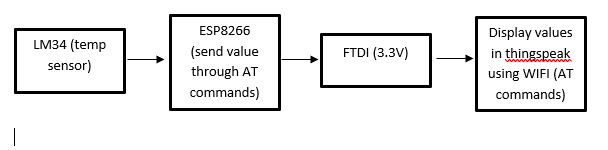


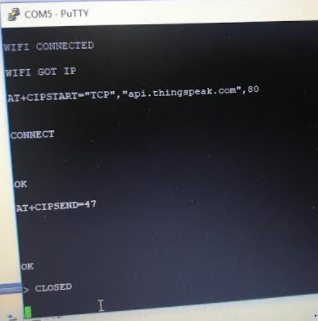


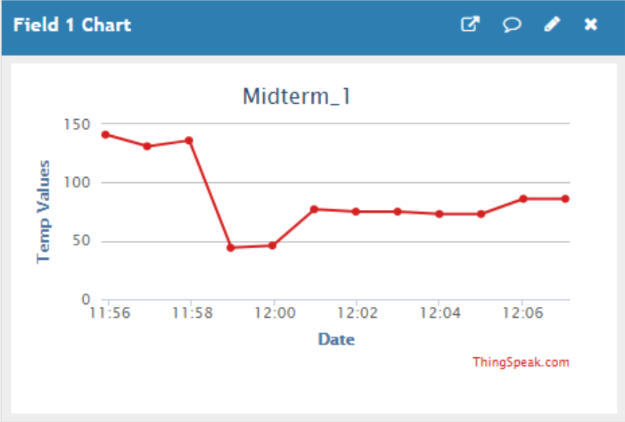
1. **SCHEMATICS**



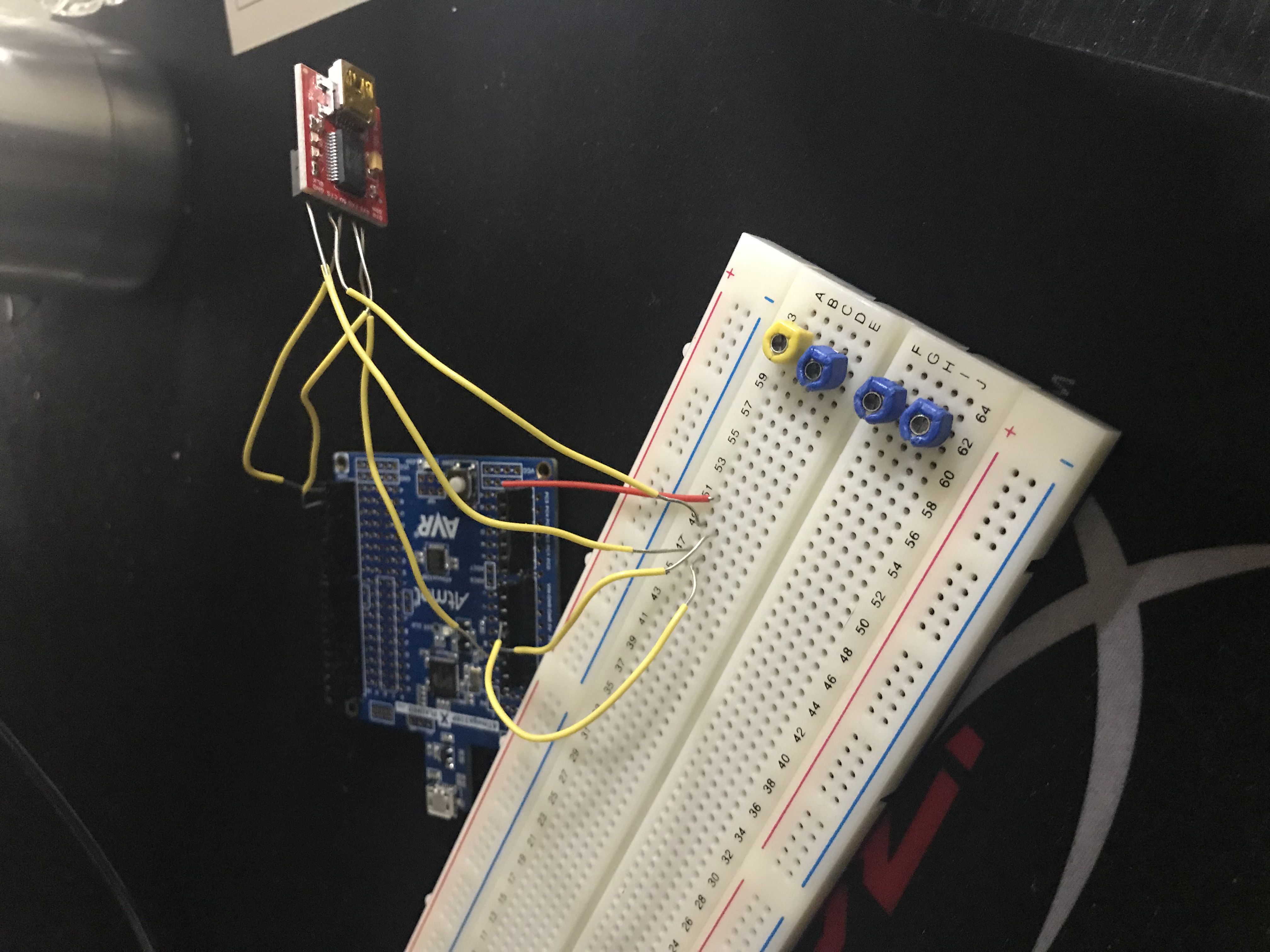
1. **SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)**

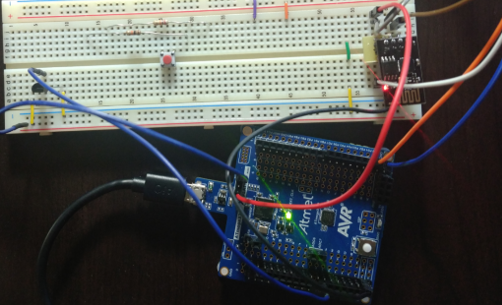






1. **SCREENSHOT OF EACH DEMO (BOARD SETUP)**





1. **VIDEO LINKS OF EACH DEMO**

Nothing To Show so decided to omit this section.

1. **GITHUB LINK OF THIS DA**
2. <https://github.com/cdaniel64/blassen>

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.